

Fig. 1A

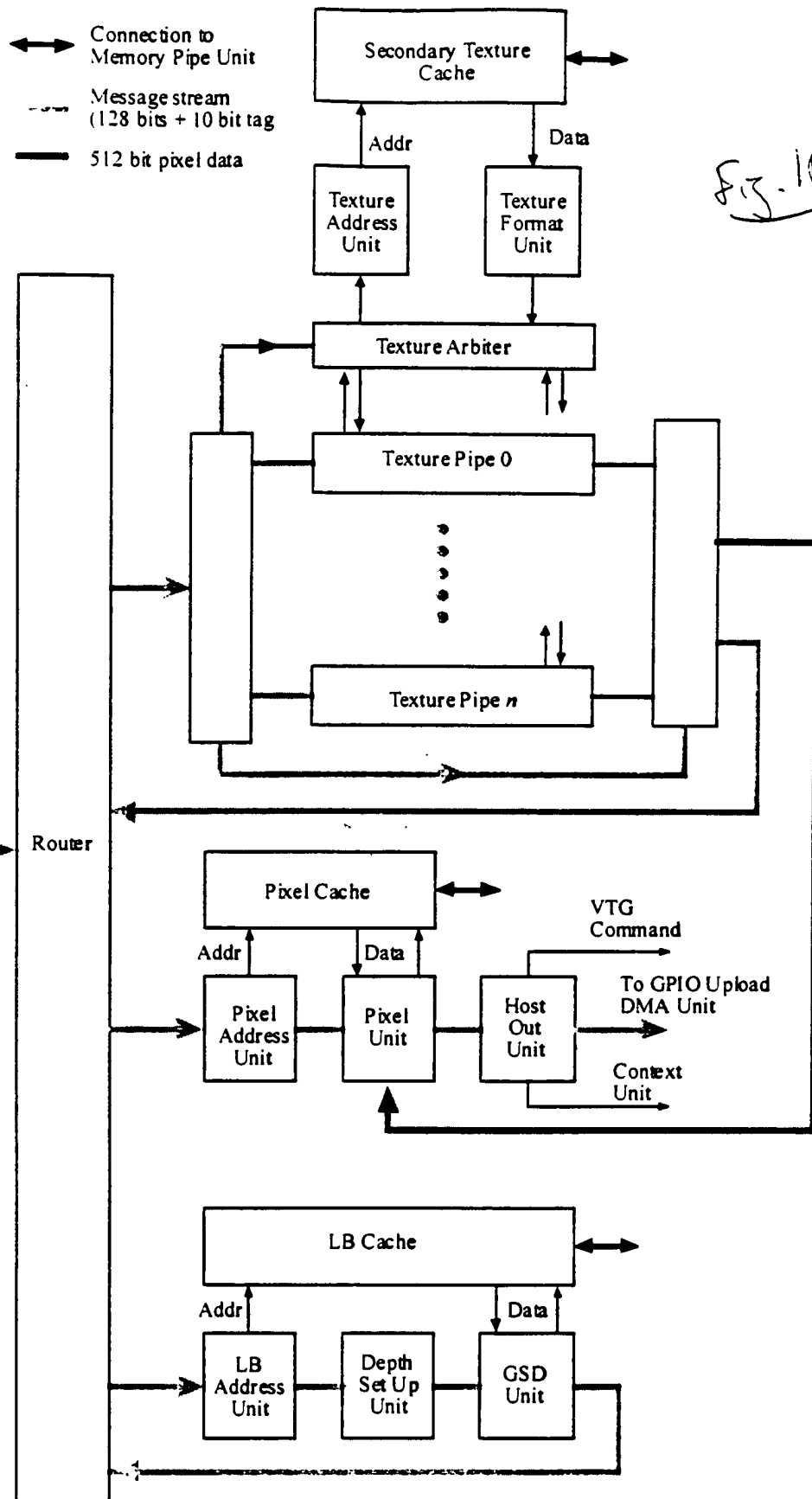


Fig. 1B

FIG. 1B is a block diagram of a graphics pipeline architecture.

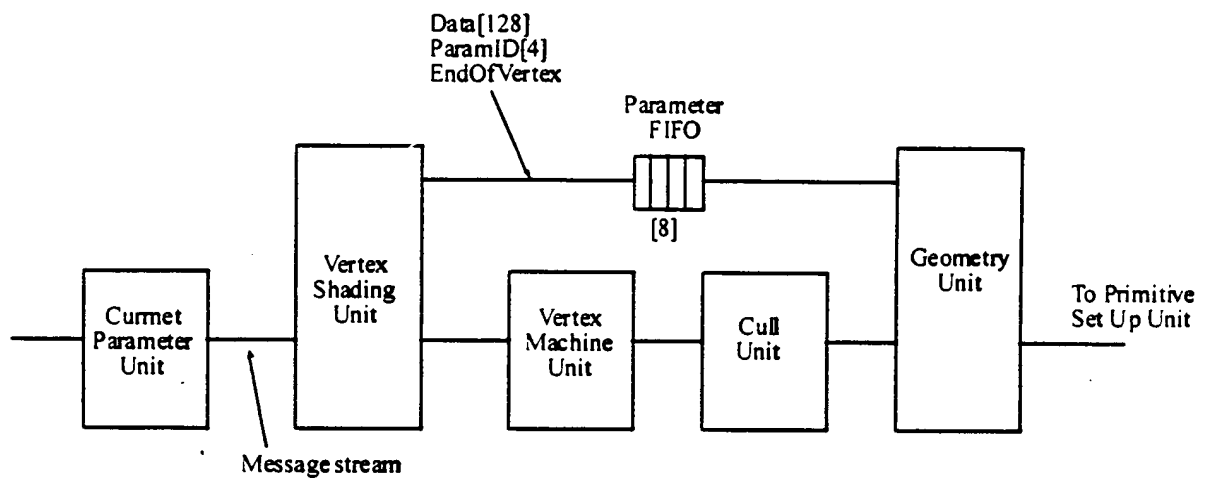


Figure 1c

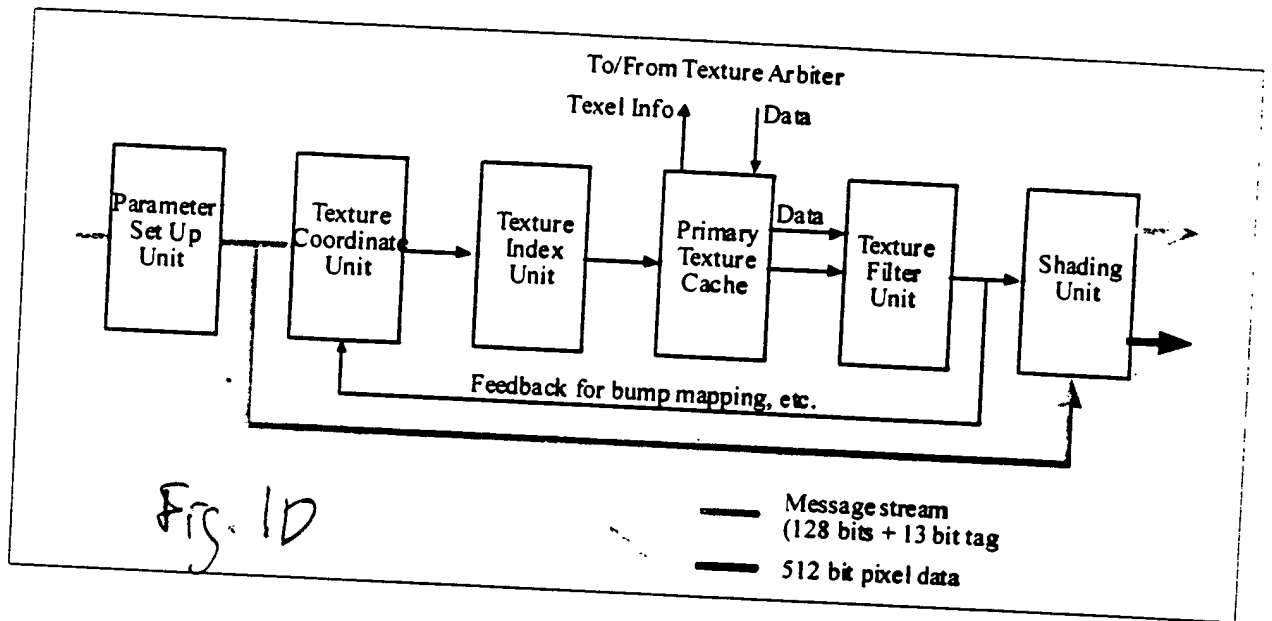
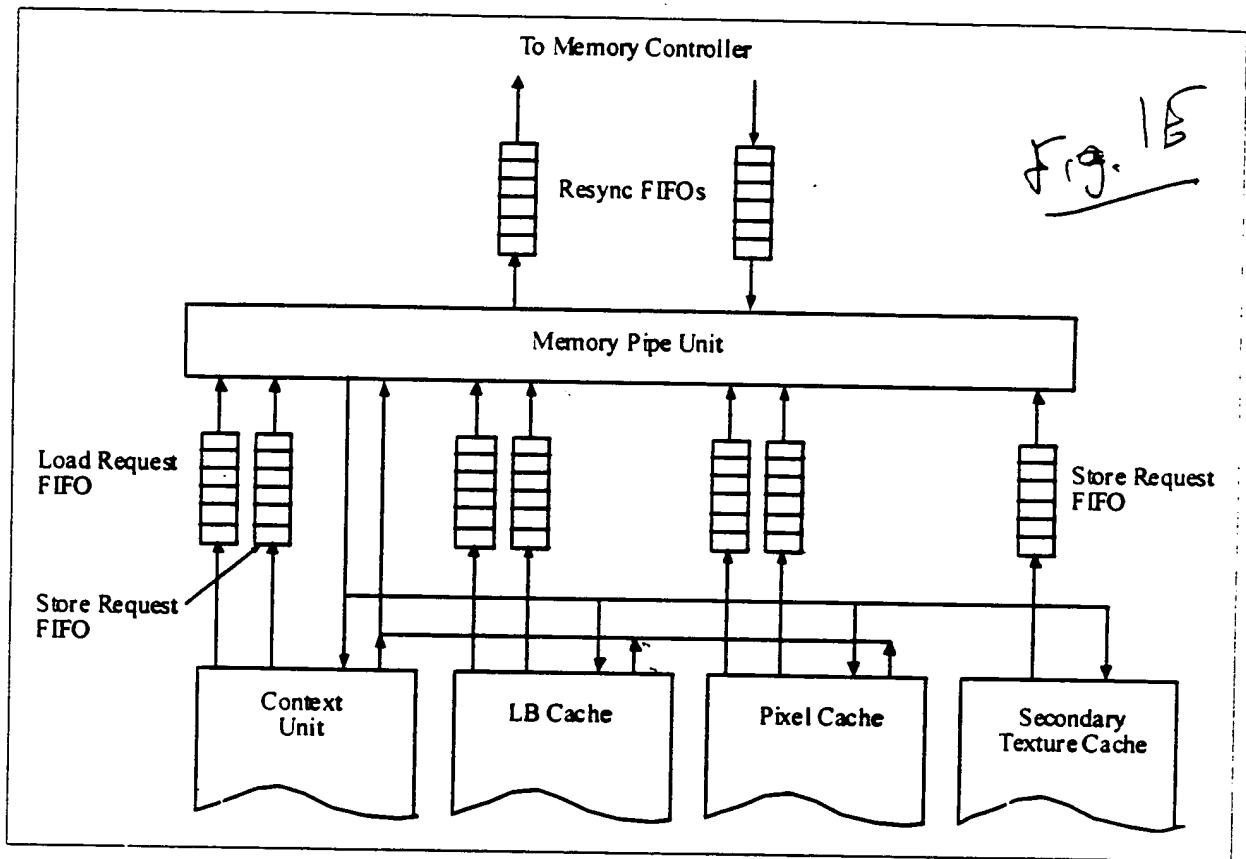


Fig. 1D



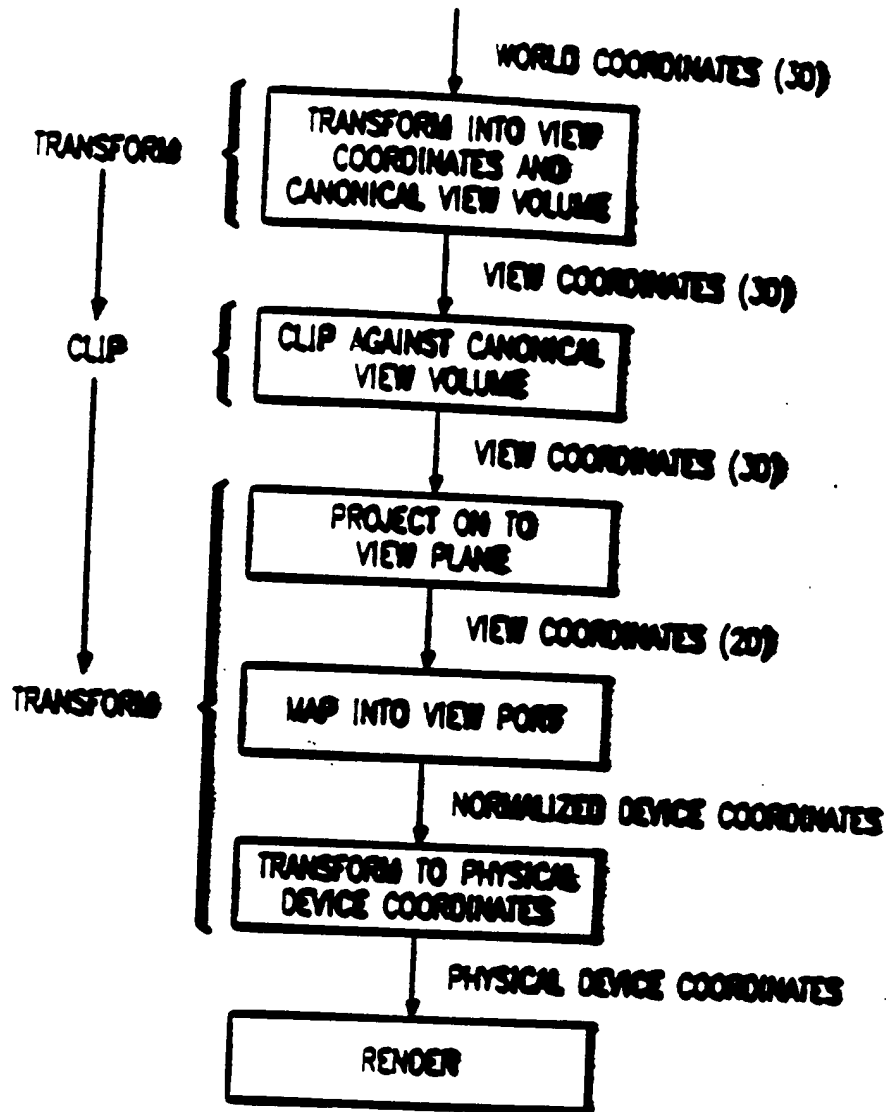


Figure 2